



Name:..... 3rd / 4th year      B.N : ....      Score : .....

1. Select The Best Answer

- (1) What is a Von Neumann machine ?
  - A. Mechanical microprocessor
  - B. First electrical microprocessor
  - C. Machine that store programs into memory**
  - D. Processor use special instruction set
- (2) The register that used to address the next instruction in code segment  
A. CS    **B. IP**    C. SS    D. CX
- (3) Protected mode memory addressing allow access to
  - A. First 1 Mbyte memory
  - B. Any memory location**
  - C. 64 kbyte segments
  - D. Code, data, stack and extra segments
- (4) In protected mode memory addressing segment registers used as
  - A. A selector to global descriptor table
  - B. A selector to local descriptor table
  - C. A segment register for real mode segments
  - D. A selector to either local or global descriptor table**
- (5) Assume a descriptor with base  $A00000H$  and a limit of  $1000H$ , what is starting and ending locations of that descriptor ?
  - A.  $A01000H - A10000H$
  - B.  $A00000H - A01000H$**
  - C.  $000000H - A01000H$
  - D. None of the above
- (6) Program invisible registers includes the following registers except
  - A. Descriptor cache registers
  - C. Paging registers**
  - B. Task register
  - D. Descriptor table register
- (7) A bus cycle is equal to ..... clock periods  
A. 8    B. 2    **C. 4**    D. 1
- (8) If  $DT/\overline{R}$  pin is logic 1 the microprocessor is in  
**A. Transmission state**    B. Receiving State    C. Hold state    D. Interrupt handling state
- (9) After the operation of adding  $(1F)H$  with  $(2A)H$ , Witch flag will be 1
  - A. Overflow
  - C. Half carry**
  - B. Carry
  - D. Zero
- (10) What type of circuit is used at the interface point of an output port?
  - A. Decoder
  - B. Latch**
  - C. Buffer
  - D. Register

- (11) What type of circuit is used to provides signal protection and amplification
- A. Decoder  
B. Latch  
C. **Buffer**  
D. Register
- (12) The control bus and memories share a bidirectional bus in a typical microprocessor system.
- A. True  
B. **False**
- (13) A register in the microprocessor that keeps track of the answer or results of any arithmetic or logic operation is the:
- A. stack pointer B. program counter C. instruction pointer D. **accumulator**
- (14) Which of the following buses is primarily used to carry signals that direct other ICs to find out what type of operation is being performed?
- A. **control bus** B. data bus C. address bus D. address decoder bus
- (15) In 8086 microprocessor one of the following statements is not true
- A. Coprocessor is interfaced in MAX mode  
B. **Coprocessor is interfaced in MIN mode**  
C. I/O can be interfaced in MAX / MIN mode  
D. Supports pipelining
- (16) In memory paging system the address generated by a program is
- A. **Linear address** B. Physical address C. Page address D. Directory address
- (17) There is no memory segmentation in
- A. Real Mode B. **Flat Mode** C. Protected Mode D. Paging system
- (18) AX register is
- A. 8 bits  
B. **16 bits**  
C. 32 bits  
D. 64 bits
- (19) Data transfer instructions will.
- A. Affect flag register  
B. **Not affect flag register**  
C. Use RSI and RDI registers  
D. Non of the above
- (20) The computer architecture aimed to reducing the time of execution of instructions is
- A. CISC  
B. **RISC**  
C. 8086  
D. ISA

With My Best Wishes  
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